

ABSTRACT

System and method for external bus device support. The system comprises a processor, a memory, one or more external bus controllers and a basic input-output system (BIOS). The BIOS contains an external bus support component to cause a periodic interrupt to be generated and to provide support for external bus enabled devices responsive to the periodic interrupt. The method comprises obtaining a portion of the memory to be used to maintain a plurality of external bus device data; causing an interrupt to be periodically generated; and handling input produced by external bus enabled devices using the portion of the memory. The interrupt may be a system management interrupt (SMI) of the 32-bit Intel Architecture (IA-32). The external bus controller may be a Universal Serial Bus (USB) host controller, the external bus devices may be USB devices, and the external bus support component may be a USB support component.